|  |
| --- |
| Embedded Systems Lab Experiment 2 |
| Digital Output through 7 segment LED with SRAM memory mapping |
| Program an ATMega 32 to control a 7-segment display |

|  |
| --- |
| Jitbitan Baroi  1-11-2024 |

|  |  |
| --- | --- |
|  |  |
| Schematic for Assignment 1, 2 An up counter | Schematic for Assignment 3 Road Traffic Signaling  (a down counter ) |

|  |
| --- |
|  |
| Schematic for assignment 4 Road Traffic Signaling, down counter using 2 displays |